

O I P E JC135
JUL 05 2005
PATENT & TRADEMARK

DFZGW

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C. 20231, on June 30, 2005.

Ravi M. S.
Attorney for Applicant

PATENT

Docket No. SJO920000065US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vladimir Nikitin et al.)
Serial No.: 10/087,332)
Filed: March 1, 2002) Group Art
For: **REDUCTION OF INTERFERENCE PICKUP IN**) Unit: 2652
HEADS FOR MAGNETIC RECORDING BY)
MINIMIZING PARASITIC CAPACITANCE)
Examiner: Davis, Donald D.)

APPELLANTS' REQUEST FOR REINSTATEMENT / MAINTAINING OF THE

APPEAL

Mail Stop Appeal Brief-Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The USPTO received Appellants' timely Appeal Brief on February 22, 2005 and Notice of Appeal on June 25, 2004. The Notice of Appeal was filed in response to the Final Office Action mailed March 24, 2004. In response to the Appeal Brief, the Examiner reopened prosecution and issued a Final Office Action mailed May 18, 2005. Based on the Final Office Action and the Examiner's reliance on 37 C.F.R. §1.193(b)(2), Appellants consider the reopened prosecution and Final Office Action an Examiner's answer under 37 C.F.R. §1.193 and 37 C.F.R. §41.39.

Appellants submit that reopening of prosecution is unnecessary and that making the first action final after reopening prosecution based on a new ground of rejection is improper. In particular, the new ground of rejection was neither necessitated by an amendment nor based on an information disclosure statement. The finality of the Office Action is consequently not supported by M.P.E.P. §1208.02. Therefore, Appellants are submitting this Request for Reinstatement of the Appeal to reinstate the appeal in accordance with 37 C.F.R. §1.193(b)(2) and maintenance of the appeal under 37 CFR 41.39(b)(2). Additionally, Appellants are submitting a Supplemental Appeal Brief to address the new rejection issued in the most recent Office Action.

The filing fee set forth in 37 C.F.R. § 1.17(c) of \$330.00 for the appeal was previously submitted. The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication, or to credit any overpayment, to Deposit Account No. 09-0466.

Respectfully submitted,

Date: June 30, 2005

Kunzler & Associates
8 E. Broadway, Suite 600
Salt Lake City, Utah 84111
Telephone: 801/994-4646



David J. McKenzie
Reg. No. 46,919
Attorney for Applicant



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on June 30, 2005.

[Signature]
Attorney for Applicant

PATENT

Docket No. SJO920000065US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vladimir Nikitin et al.)
Serial No.: 10/087,332)
Filed: March 1, 2002) Group Art
For: **REDUCTION OF INTERFERENCE PICKUP IN**) Unit: 2652
HEADS FOR MAGNETIC RECORDING BY)
MINIMIZING PARASITIC CAPACITANCE)
Examiner: Davis, Donald D.)

APPELLANTS' SUPPLEMENTAL APPEAL BRIEF

Mail Stop Appeal Brief-Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The USPTO received Appellants' timely Appeal Brief on February 22, 2005 and Notice of Appeal on June 25, 2004. The Notice of Appeal was filed in response to the Final Office Action mailed March 24, 2004. In response to the Appeal Brief, the Examiner reopened prosecution and issued a Final Office Action mailed May 18, 2005. The Final Office Action included a new ground of rejection, but was not necessitated by an amendment or based on an information disclosure statement. Based on the Final Office Action and the Examiner's reliance on 37 C.F.R. §1.193(b)(2), Appellants consider the reopened prosecution and Final Office Action a written statement in answer to Appellant's brief (an Examiner's answer) under 37 C.F.R. §1.193(b)(2) and 37 C.F.R. §41.39. Therefore, Appellant is submitting this supplemental appeal brief in compliance with 37 CFR 41.39(b)(2).

In response, Appellants are filing this Supplemental Appeal Brief with the accompanying Request for Reinstatement/Maintaining of the Appeal. Appellants submit that reopening prosecution is an unnecessary waste of time and money, and that making the first action final based on a new ground of rejection is improper. New grounds of rejection are addressed below in Section 7 as set forth in 37 C.F.R. §41.37(c)(1)(vii). Appellants reiterate all of the applicable arguments presented in Appellants Appeal Brief of February 22, 2005.

1. REAL PARTY IN INTEREST

The corresponding part of the Appeal Brief of February 22, 2005 is applicable and is incorporated by reference.

2. RELATED APPEALS AND INTERFERENCES

The corresponding part of the Appeal Brief of February 22, 2005 is applicable and is incorporated by reference.

3. STATUS OF CLAIMS

The Final Office Action mailed March 24, 2004 rejected claims 1-25 under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 4,807,073 to Takeura et al. (hereinafter “Takeura”) in view of U.S. Patent No. 5,048,175 to Jurisch et al. (hereinafter “Jurisch”). Given that the Examiner failed to cite Takeura and Jurisch in the new Final Office Action mailed May 18, 2005, Appellants consider these rejections withdrawn. Nevertheless, to the extent that the Examiner might argue that these rejections are still relevant, Appellants maintain and incorporate by reference the arguments presented in the Appellant’s Appeal Brief of February 22, 2005.

The new Final Office Action mailed May 18, 2005 issued a new ground of rejection in response to the Appeal Brief filed on February 22, 2005. The new Final Office Action rejected claims 1-3, 5-7, 14, 16, and 23-25 under 35 U.S.C. § 102(b) as anticipated by U.S. Patent No. 5,966,800 to Huai et al. (hereinafter “Huai”) and also rejected claims 4, 8-13, 15, and 17-22 under 35 U.S.C. § 103(a) as obvious in view of Huai.

4. STATUS OF AMENDMENTS

Appellants filed an amendment subsequent to receipt of the final rejection mailed March 24, 2004. The amendment was entered for purposes of this appeal, as noted in the Advisory Action mailed June 16, 2004. A copy of the claims is included in section 9, Claim Appendix.

In the Appeal Brief received by the USPTO February 22, 2005, Appellants proposed an amendment to claim 15 to replace “~~Further comprising an electrical contact having~~” with “wherein the electrical contact pad has.” This amendment was proposed to address readability of the preamble and antecedent basis agreement with Claim 1, but was apparently not entered. However, since this proposed amendment is now not permitted under 37 C.F.R. 41.39(b)(2) or 37 C.F.R. 41.37(c), Appellants have removed the proposed amendment and request that the informality relating to antecedent basis be addressed once Claim 15 is allowed.

5. SUMMARY OF CLAIMED SUBJECT MATTER

Magnetoresistive (MR) and giant magnetoresistive (GMR) heads used in data storage drives may be subject to interference noise. This noise may reduce the quality of the data read from a tape storage device or a hard disk drive. As the interference noise increases, the signal-to-noise ratio (SNR) decreases and the quality of the detected read signal decreases. *See*, Background, pages 1-2. The interference noise may be due, at least in part, to ambient radio frequency (RF) energy, which may originate from external (e.g., radio and/or television station broadcasts) or internal (e.g., storage drive motors and/or electronics) sources. Summary, page 4, lines 7-15. Various embodiment disclosed in the present application reduce the effects of interference noise by reducing the capacitance of various head elements within a storage drive. Summary, page 4, lines 16-19.

One embodiment includes a magnetic head 600 having a material 602, which has a low dielectric constant, interposed between a substrate 614 and an electrical contact pad 610. The electrical contact pads 610 are the read and write elements through which a storage drive reads and writes data to/from the storage device. The low dielectric material 602 reduces the parasitic capacitive coupling between the substrate 614 and the contact pad 610, thereby improving the quality of the signal at the contact pad 610. Detailed Description, page 10, line 26 through page 11, line 12. In particular, independent claim 1 recites an electrical contact pad, a substrate, an

insulating undercoat, and a low dielectric material. The electrical contact pad 610 represents one embodiment of the electrical contact pad recited in claim 1. The substrate 614 represents one embodiment of the substrate recited in claim 1. The insulating undercoat 608 represents one embodiment of the insulating undercoat recited in claim 1. The low dielectric material 602 represents one embodiment of the low dielectric material recited in claim 1.

Another embodiment includes a magnetic head 600 having a contact pad 610 of a reduced size. The reduced surface area of the contact pad 610 minimizes the parasitic capacitance between the substrate 614 and the contact pad 610. Detailed Description, page 10, lines 5-14. In particular, claim 22 recites a substrate and a contact pad of reduced size. The substrate 614 represents one embodiment of the substrate recited in claim 22. The electrical contact pad 610 represents one embodiment of the contact pad recited in claim 22.

Another embodiment includes a magnetic head 600 having a contact pad 610 that is separated from the substrate 614 by an insulating undercoat 608 of increased thickness. Detailed Description, page 10, lines 15-22. In particular, claim 24 recites a substrate, an insulating undercoat layer, an electrical contact pad, and another layer of SiO₂. The substrate 614 represents one embodiment of the substrate recited in claim 24. The insulating undercoat 608 represents one embodiment of the insulating undercoat layer and the additional layer of SiO₂ recited in claim 24. The electrical contact pad 610 represents one embodiment of the electrical contact pad recited in claim 22.

6. GROUNDS OF REJECTION TO BE REVIEWED ON APPEAL

Issue I is addressed in the Appellant's Appeal Brief of February 22, 2005 and incorporated by reference.

I. Whether the Examiner failed to establish a *prima facie* case of obviousness under 35 U.S.C. § 103(a) for claims 1-25 where the limitations of the claims are not taught within the combination of cited references?

Issues II and III address the new rejections raised in the new Final Office Action.

II. Whether the Examiner failed to establish a *prima facie* case of anticipation under 35 U.S.C. § 102(b) for claims 1-3, 5-7, 14, 16, and 23-25 where the limitations of the claims are not taught by the cited reference?

III. Whether the Examiner failed to establish a *prima facie* case of obviousness under 35 U.S.C. § 103(a) for claims 4, 8-13, 15, and 17-22 where the limitations of the claims are not taught or suggested within the combination of cited references?

7. ARGUMENT

I. **The Examiner failed to establish a *prima facie* case of obviousness because the cited references, either alone or in combination, do not teach or suggest all of the limitations of claims 1-25.**

The corresponding part of the Appeal Brief of February 22, 2005, to the extent it may be applicable, is incorporated by reference.

II. **The Examiner failed to establish a *prima facie* case of anticipation under 35 U.S.C. § 102(b) for claims 1-3, 5-7, 14, 16, and 23-25 where the limitations of the claims are not taught by the cited reference.**

Appellants respectfully assert that Huai fails to teach all the elements of independent claims 1, 16, 23, 24, and 25. Specifically, Huai fails to teach the electrical contact pad recited in the claims. Furthermore, even if *arguendo* Huai did explicitly or inherently disclose an electrical contact pad, Huai fails to teach the insulating undercoat and low dielectric material positioned, relative to the electrical contact pad, as recited in the claims.

A. The Electrical Contact Pad

Embodiments and representations of the electrical contact pads are shown in Figures 2 (items 206 and 218), 5 (items 502), and 6 (items 606 and 610) of the present application. These read/write contact pads provide locations at which electrical leads may be connected at the read/write head. This exemplary connectivity function is depicted in Figure 5, which represents

the contact pads 502 as connection points for the various lead wires (not shown), e.g. W+, W-, R+, and R-. Furthermore, Figures 2, 5, and 6 each depict the contact pads as being separate from the magnetoresistive (MR) coil. These contact pads are particularly distinct from any electrical windings within the MR coil.

The Office Action states that Huai discloses an electrical pad 36, but this mischaracterizes the Huai disclosure. Item 36 of Huai is an electrical feedthrough that connects the lower and upper inductive coils 32A, 32B together. Huai, Fig. 5; col. 4, lines 12-17. Therefore, the electrical feedthrough 36 is an intrinsic part of the coil structure, providing a serial connection between the lower and upper coils 32A, 32B. In fact, the electrical feedthrough is nothing more than a part of the two coil layers 32A, 32B.

As described in Huai, the lower coils 32A, including the lower portion of the electrical feedthrough 36, are formed during the electroplating process shown in Figure 6L. Subsequently, the upper coils 32B, including the upper portion of the electrical feedthrough 36, are formed during a substantially similar electroplating process shown in Figure 6O. The upper portion of the electrical feedthrough 36 is physically deposited on the lower portion of the electrical feedthrough 36 via the feedthrough 62 void shown in Figure 6N. See, Huai, col. 5, lines 40-67. Therefore, it is clear that the electrical feedthrough 36 is not anticipatory of the electrical contact pad of the present application because it is an integral part of the coil structure and does not function to connect lead wires to the MR head. Rather, the electrical feedthrough 36 simply connects the lower coils 32A to the upper coils 32B. See Huai, col. 5, lines 59-62.

B. The Insulating Undercoat

The Office Action states that Huai discloses an insulating alumina undercoat 56, but this also mischaracterizes the Huai disclosure. Item 56 of Huai is only described as a protective layer. Huai, col. 5, lines 23-30. While Huai does explain that a slurry of alumina or silicon dioxide may be used for gross removal of excess protective layer material, Huai does not disclose the composition of the protective material itself. In other words, Huai describes a protective material 56 and a separate slurry that may be used to conduct preliminary leveling of the protective layer before fine leveling by ion milling. The alumina slurry, however, is distinct from the protective material 56, which has an undisclosed composition. Therefore, the protective

material 56 does not anticipate the insulating undercoat of the present application because its composition and insulating properties, if any, are undisclosed.

C. The Low Dielectric Material

The Office Action states that Huai discloses a material 60 & 66 selected to have a low dielectric constant interposed between the pad 36 and the insulating alumina undercoat 56. As described above, the Office Action's references to a "pad 36" and an "insulating alumina undercoat 56" are not supported by Huai. Nevertheless, even if arguing the electrical feedthrough 36 were a contact pad and the protective layer 56 were an insulating undercoat, Huai still fails to disclose a low dielectric material interposed between the electrical feedthrough 36 and the protective layer 56.

Rather, Huai specifically discloses the opposite configuration—Huai describes and shows the electrical feedthrough 36 interposed between the dielectric material 66 and the protective layer 56. See Huai Figures 5 and 6S. The dielectric layer 60 is on the side of the electrical feedthrough 36, also interposed between the dielectric material 66 and the protective layer 56. Appellant notes that Figure 6S illustrates the feedthrough 36 using two layers of cross-hatch to explain the manufacturing process, but together these layers form the feedthrough 36. There is no way to accurately describe the configuration shown in Figure 5 of Huai to support the Office Action's assertion that either of the dielectric materials 60, 66 is interposed between the electrical feedthrough 36 and the protective layer 56 because the electrical feedthrough 36 and protective layer 56 are immediately adjacent to one another. See Huai Figure 6S. Therefore, the dielectric materials 60, 66 do not anticipate the low dielectric material interposed between the pad and the substrate.

For the reasons stated above, Huai fails to teach all of the elements recited in the independent claims 1, 16, 23, 24, and 25. In particular, Huai fails to teach the recited electrical contact pad, the insulating undercoat, and the low dielectric material. Accordingly, the Office Action fails to establish a *prima facie* case of anticipation because the cited reference fails to teach every element of these claims. Given that the cited reference fails to teach all of the elements recited in claims 1, 16, 23, 24, and 25, Applicant respectfully submits that independent claims 1, 16, 23, 24, and 25 are patentable over the cited reference. Consequently, Applicant requests that the rejection of claims 1, 16, 23, 24, and 25 under 35 U.S.C. § 102(b) be withdrawn.

Given that dependent claims 2-15 depend from claim 1, Applicant respectfully submits that claims 2-15 are also patentable over the cited reference. Accordingly, Applicant requests

that the rejection of dependent claims 2-3, 5-7, and 14 under 35 U.S.C. § 102(b) be withdrawn. Furthermore, Applicant requests that the rejection of dependent claims 4, 8-13, and 15 under 35 U.S.C. § 103(a) also be withdrawn.

III. The Examiner failed to establish a *prima facie* case of obviousness under 35 U.S.C. § 103(a) for claims 4, 8-13, 15, and 17-22 where the limitations of the claims are not taught or suggested within the combination of cited references.

Appellants respectfully assert that Huai fails to teach or suggest all the elements of independent claims 17 and 22. Specifically, Huai fails to teach or suggest the electrical contact pad recited in the claims. Furthermore, even if *arguendo* Huai explicitly or inherently discloses an electrical contact pad, Huai fails to teach or suggest the insulating undercoat and low dielectric material positioned, relative to the electrical contact pad, as recited in claim 17. Furthermore, Huai fails to teach or suggest a surface area characteristic of the electrical contact pad, as recited in claim 22.

As described above, with regard to the rejections under 35 U.S.C. § 102(b), Huai fails to teach an electrical contact pad, an insulating undercoat, or a low dielectric material, as recited in claim 17. Specifically, the electrical feedthrough 36 of Huai does not anticipate the electrical contact pad of the present application. Similarly, the protective layer 56 of Huai does not anticipate the insulating undercoat of the present application. Furthermore, the dielectric materials 60, 66 do not anticipate the low dielectric material of the present invention.

With regard to the surface area of the electrical contact pad recited in claim 15, the Office Action completely fails to provide any support for the assertion of obviousness. Even if *arguendo* the electrical feedthrough 36 of Huai were an electronic contact pad, as recited in the present application, Huai still does not make obvious the surface area of the electrical feedthrough 36. In fact, Huai is silent as to the surface area of the electrical feedthrough 36 and provides absolutely no particular guidance as to the design, shape, or dimensions of the electrical feedthrough 36.

Moreover, the Office Action fails to provide any motivation to modify the electrical feedthrough 36 of Huai to have a particular surface area. The Office Action only states that

modifying the electrical feedthrough 36 so that it has a surface area of less than 20 μm would “optimize the electrical properties of the [electrical feedthrough 36] and decrease any unwanted interference.” This motivation is not present in Huai. Although not clearly stated, the Examiner appears to improperly take Official Notice of this assertion, but fails to comply with the requirements for Official Notice. Specifically, the assertion is not shown to be well-known or common knowledge in the art and the brief, conclusory statement fails to provide a clear and unmistakable technical line of reasoning, as required by M.P.E.P. § 2144.04. If the Examiner persists with this assertion, Applicant requests that the Examiner provide evidence to show that modifying the electrical feedthrough 36 of Huai to have a surface area of less than 20 μm would in fact “optimize the electrical properties of the [electrical feedthrough 36] and decrease any unwanted interference.” Without the proper evidentiary support for this conclusory assertion, the Examiner is understood to have improperly relied on impermissible hindsight to produce an otherwise unsupported motivation to modify the cited reference.

For the reasons stated above, Huai fails to teach or suggest all of the elements recited in the independent claims 17 and 22. In particular, Huai fails to teach or suggest the recited electrical contact pad, the insulating undercoat, and the low dielectric material. Huai also fails to teach or suggest the surface area characteristic of the electrical contact pad. The Examiner also fails to show adequate motivation to modify Huai.

Accordingly, the Office Action fails to establish a *prima facie* case of obviousness because the cited reference fails to teach every element of these claims or show a suggestion or motivation to modify the cited reference. Given that the cited reference fails to teach all of the elements recited in claims 17 and 22, Applicant respectfully submits that independent claims 17 and 22 are patentable over the cited reference. Consequently, Applicant requests that the rejection of claims 17 and 22 under 35 U.S.C. § 103(a) be withdrawn.

Given that dependent claims 18-21 depend from claim 17, Applicant respectfully submits that claims 18-21 are also patentable over the cited reference. Accordingly, Applicant requests that the rejection of dependent claims 18-21 under 35 U.S.C. § 103(a) be withdrawn.

SUMMARY

In view of the foregoing, each of the claims on appeal has been improperly rejected because the Examiner has not properly established a *prima facie* case of anticipation or a *prima facie* case of obviousness for claims 1-25. Appellants submit that the foregoing arguments establish the novelty and non-obviousness of the claims over the cited reference. Therefore, Appellants respectfully request reversal of the Examiner's rejections under 35 U.S.C. §§ 102(b) and 103(a). Furthermore, Appellants request allowance of pending claims 1-25.

Respectfully submitted,

Date: June 30, 2005

Kunzler & Associates
8 E. Broadway, Suite 600
Salt Lake City, Utah 84111
Telephone: 801/994-4646

David J. McKenzie
Reg. No. 46,919
Attorney for Applicant

8. CLAIM APPENDIX

The claims involved in the appeal, namely claims 1-25, are listed below.

1. A magnetic head comprising:
an electrical contact pad;
a substrate on which the magnetic head is formed;
an insulating undercoat interposed between the pad and the substrate; and
a material selected to have a low dielectric constant interposed between the pad
and the insulating undercoat.
2. The magnetic head of claim 1, wherein the low dielectric material is configured to
decrease the parasitic capacitance of the magnetic head.
3. The magnetic head of claim 1, further comprising a stud formed through the low
dielectric material.
4. The magnetic head of claim 3, wherein the stud comprises Cu.
5. The magnetic head of claim 3, wherein the stud comprises a conductive material.
6. The magnetic head of claim 1, wherein the low dielectric material comprises hard-
bake photo resist.
7. The magnetic head of claim 1, wherein the low dielectric material comprises SiO₂.

8. The magnetic head of claim 1, wherein the low dielectric material has a thickness in a range of between about 1 μm and about 100 μm .

9. The magnetic head of claim 1, wherein the low dielectric material has a thickness in a range of between about 10 μm and about 50 μm .

10. The magnetic head of claim 1, wherein the low dielectric material has a thickness of about 20 μm .

11. The magnetic head of claim 1, wherein the low dielectric material has a dielectric constant of less than about 9.

12. The magnetic head of claim 1, wherein the low dielectric material has a dielectric constant of about 3.

13. The magnetic head of claim 1, wherein the magnetic head carries a GMR sensor.

14. The magnetic head of claim 1, wherein the low dielectric material provides a platform for the electrical contact pad.

15. The magnetic head of claim 1, Further comprising an electrical contact pad having a surface area of less than about 20 μm in order to reduce capacitance coupling with the substrate.

16. A reduced capacitance magnetic head comprising:
- an electrical contact pad;
 - a substrate on which the magnetic head is formed;
 - an insulating layer formed over the substrate;
 - a low dielectric material interposed between the pad and the substrate which is used as a platform for the electrical contact pad to increase the distance between the substrate and the electrical contact pad, the low dielectric material comprising hard bake photo resist and having a thickness of about 20 μm and a dielectric constant of about 3;
 - and
 - a conducting stud formed through the low dielectric material to make electrical connection between the electrical contact pad and the insulating layer.
17. A disk drive system, comprising:
- a reduced capacitance magnetic head comprising:
 - an electrical contact pad;
 - a substrate on which the magnetic head is formed;
 - an insulating undercoat interposed between the pad and the substrate;
 - a material selected to have a low dielectric constant interposed between the pad and the insulating undercoat; and
 - a magnetic recording disk;
 - a spin-valve sensor for reading data recorded on the recording disk; and
 - an actuator for moving the spin valve sensor across the magnetic recording disk in order for the spin-valve sensor to access different magnetically recorded data on the magnetic recording disk; and

a detector electrically coupled to the spin-valve sensor and configured to detect changes in resistance of the sensor caused by rotation of the magnetization of the sensing layer relative to the fixed magnetizations of the pinned layer in response to changing magnetic fields induced by the magnetically recorded data.

18. The disk drive system of claim 17, further comprising a stud formed through the low dielectric material.

19. The disk drive system of claim 17, wherein the low dielectric material is configured to decrease the parasitic capacitance of the magnetic head.

20. The disk drive system of claim 17, wherein the low dielectric material has a thickness in a range of between about 10 μm and about 50 μm .

21. The disk drive system of claim 17, wherein the magnetic head comprises a GMR sensor.

22. A reduced capacitance magnetic head comprising:
a substrate on which the magnetic head is formed; and
a contact pad disposed above the substrate and having a surface area less than about 20 μm in order to reduce capacitance coupling with the substrate.

23. A magnetic head comprising:
a substrate on which the magnetic head is formed;
an alumina undercoat layer comprising Al_2O_3 formed over the substrate;

an electrical contact pad; and
a layer of alumina interposed between the electrical contact pad and the alumina undercoat layer.

24. A magnetic head comprising:
a substrate on which the magnetic head is formed;
an insulating undercoat layer comprising SiO₂ formed over the substrate;
an electrical contact pad; and
a layer of SiO₂ interposed between the electrical contact pad and the insulating undercoat layer.

25. A method of reducing capacitance in a magnetic head, comprising:
providing a substrate;
providing an insulating layer directly over the substrate;
providing a read/write head; and
providing a material selected to have a low dielectric constant between the pad and the insulating layer for isolating the read/write head from the substrate in order to reduce the capacitance coupling between the read head and the substrate.